

Amendments to the Specification:

Please **replace** paragraph [0012] with the following amended paragraph:

Figure 5 is a block diagram showing relevant components of PCI-Express devices 64 and 66 coupled to respective PCI-Express buses 72. The PCI-Express devices shown in Figure 5 are the PCI devices shown in Figure 2 3 with the PCI interface circuits 42 and 52 replaced by PCI-Express interface circuits 82 and 92, respectively. PCI-Express interface circuit 82 is substantially different than the PCI interface circuit 42, and PCI-Express interface circuit 92 is substantially different than the PCI interface circuit 52. The signaling and protocol on internal buses 44 and 54 are different from the signaling and protocol of PCI-Express buses 72. PCI-Express interface 82 translates the difference in signaling and protocol in PCI-Express bus 72 and internal bus 44, while PCI-Express interface 92 translates the difference in signaling and protocol in PCI-Express bus 72 and internal bus 54. In other words, PCI-Express interface 82 translates data received from core circuit 40 in a format for transmission over internal bus 44, into data formatted for transmission over PCI-Express bus 72 and vice versa, while PCI-Express interface 92 translates data received from core circuit 50 in a format for transmission over internal bus 54, into data formatted for transmission over PCI-Express bus 72 and vice versa. The PCI-Express interfaces operate according to the signaling and protocol of a PCI-Express Specification and enable the core circuits 40 and 50 to communicate with other devices.